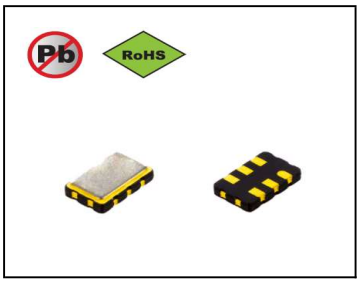
KLV5 SMD VCXO



■ **LVDS VCXO 6PAD 5.0x3.2**

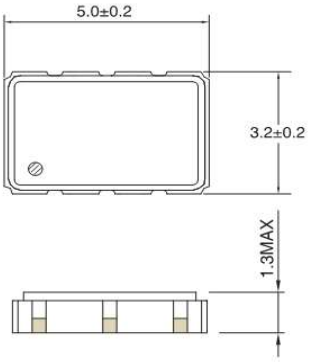
- Miniature Ceramic Surface Mount package
- Wide pulling range and Low phase noise
- Fundamental crystal design

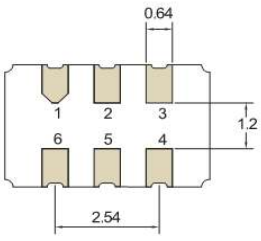
● Applications : Telecommunications, Network equipment

■ Standard Specification

Frequency range		10MHz to 250MHz
Supply voltage		3.3V
Pin1 Control Voltage		1.65V±1.35V
Frequency Pull Range		±50ppm Min or please specify
Linearity		10% Max
Frequency slope		Positive
Current consumption		35mA Max or please specify
Frequency Stability		±25ppm, ±50ppm, ±100ppm
Operating Temperature Range		-20 to 70℃, -40 to 85℃
Storage Temperature Range		-55 to 125℃
Output load condition		100Ω (OUT - OUTN)
Symmertry (Duty)		50% ±10%
Output rise and fall time (Tr /Tf)		0.7ns Max
Output high level		1.43V Typ ~ 1.60V Max
Output low level		0.90V Min ~ 1.10V Typ
Differential output voltage		0.247V ~ 0.454V, 0.350V Typ
Offset Voltage		1.125 ~ 1.375V, 1.250V Typ
Pin 2 Function	Enable input voltage	70% Vdd Min or N.C
	Disable input voltage	30% Vdd Max
Oscillation start up time		5ms Max
Aging (Initial first year)		± 3ppm Max
Phase jitter (12KHz ~ 20MHz)		89 fs typ at 122.88MHz (3.3V)

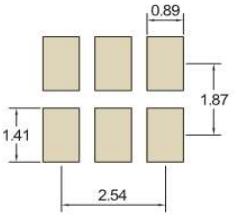
■ Dimensions (mm)





CONNECTION
#1 V.C
#2 Tri-State
#3 GND
#4 OUTPUT
#5 COMP-OUTPUT
#6 Vdd

• Recommended Solderring Pattern



■ Part Numbering Guide

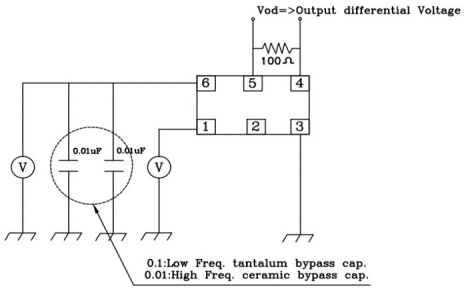
KLV5 3 50 C D A50 S2 R 27.000000

- ▶Package Type
- ▶Supply Voltage
  (3) : 3.3Vdc (X) : Custom
- ▶Frequency Stability
  (20) : ±20ppm (25) : ±25ppm
  (50) : ±50ppm (BLANK) : ±100ppm
- ▶Operating Temperature Range
  (A) : 0/70℃ (B) : -20/70℃ (C) : -40/85℃
- ▶Duty Cycle
  (D) : 40/60% (T) : 45/55%
- ▶Minimum Pull Range
  (BLANK) : ±50ppm (100) : ±100ppm
  (A50) : APR ±50ppm (A80) : APR ±80ppm
- ▶Enable/Disable Pin
  (S2) : Pin2 E/D (BLANK) : No connection
- ▶Packing Type
  (B) : Bluk (R) : Tape & Reel [1,000pcs]
- ▶Output Frequency : xxx.xxxxxxMHZ

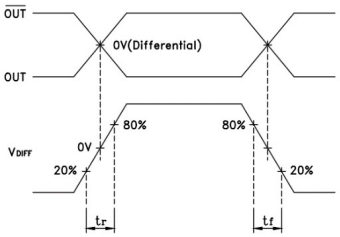
*Please contact us for other specifications.

■ Test Circuit and Waveform

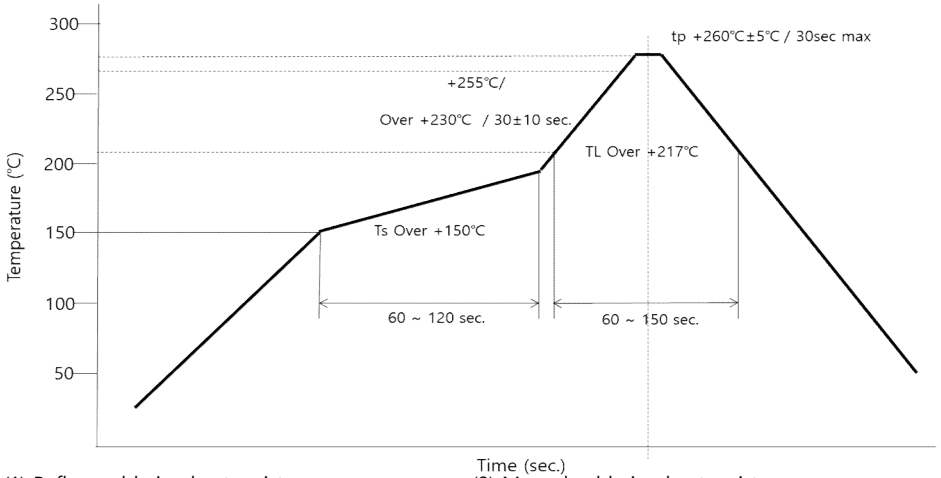
LVDS Test Circuit



LVDS Transition Time Waveform



■ Recommendation reflow condition



(1) Reflow soldering heat resistance
Peak Temp. : 265℃, 30sec max.
Heating : 230℃ or higher, 40sec.
Preheating : 150~180℃, 120sec.
Reflow passage times : 2 times

(2) Manual soldering heat resistance
Pressing a soldering iron of 350°C on the terminal electrode
Soldering touch time : 4sec.
Manual soldering times : 2 times